School of Informatics, **NORTHERN ARIZONA** UNIVERSITY Cyber Systems



1. Introduction

Scanning Tunneling Microscope (STM) is an instrument used to obtain images of the surface of a material at atomic resolution.

2. Requirements

- \succ Resonant frequency of the mechanical system should be <1Hz.
- \succ Voltage amplifiers to produce ±15 V to produce a resolution of ~0.01 nm in the Z direction.
- \succ Noise figure should be < 3 nV/ \sqrt{Hz} for the amplifier.

3. Problem Statement

- > Cost of industrial STM can cost up to \$500,000, making it financially inaccessible.
- > Construct a STM with commercially available components and a limited budget to be used for future research of nanoelectronics and Northern Arizona University.

4. Challenges



Figure 1. + and -x signal from the piezo driver circuit



Figure 3. SEM image of one of the tungsten scanning tips that got **x- \y+** bent in the etching process



Figure 2. Piezo disk connected to a breakout board, using silver

paste to solder contacts.

Tube Figure scanner: requires large voltages to achieve the same displacement as a disk scanner





[1] [2]

Inexpensive Scanning Tunneling Microscope for Academic Research

Atlántida Félix, Loren Larrieu, & Matthew Miller School of Informatics, Computing, & Cyber Systems, Northern Arizona University

5. Solution Statement

> Methods



Figure 5. Piezo disk showing how auadrants should be divided, and heir movement.



Figure 7. Schematic of the analog PI controller.



Figure 6. CAD model of the final design of the mechanical structure





mechanical structure, with piezo disk integrated.



Figure 10. Final version of piezo disk with tip placement.

6. Architecture



12. References

"IBM100- Scanning Tunneling Microscope." [Online]. Available: https://www.ibm.com/ibm/history/ibm100/us/en/icons/microscope/. Accessed 25-Nov-2022.

J. Tersoff and N. D. Lang, "1. Theory of Scanning Tunneling Microscopy," in methods in Experimental Physics, vol. 27, Elsevier, 1993, pp. 1-29. doi: 10.1016/S0076-695X(08)60006X.

[3] [4] [5] Available:









Figure 11. Schematic of the preamplifier.

C. J. Chen "11.1 Current Amplifier" and "11.2 Feedback circuit", in Introduction to Scanning Tunneling Microscopy. New York, NY, United States: Oxford University Press, 1993, chp. 11, sec. 1 & 2, pp. 251-266.

D. Berard, "Electronics," Dan Berard, 29-Dec-2021. [Online] Available: https://dberard.com/home-built-stm/electronics/. Accessed 25-Nov-2022.

J. D. Alexander, "Disk Scanner," Disk_Scanner _EXP, 2000. [Online]. https://john-alexander42.github.io/simple-stm-webpage/Disk_Scanner_Exp.htm. [Accessed: 20-Apr-2023].

7. Testing

integrated system.

into a photo at a later date.

- electronics or related fields.

- device fabrication and research
- structure isolation

Thanks to Dr. Carlo da Cunha and our GTA mentor Jordan Beverly, for their guidance throughout this project.

Thanks, also to The NAU Engineering department for funding our project, and to Dr. Stephanie Hurst and all members of the Hurst group, especially Ms. Madison King, for their assistance and for providing access to their laboratory.

Center for Quantum Networks National Science Foundation Engineering Research Center

 \succ Each sub-module was tested first separately, then once again during the assembly of the

8. Results

 \succ The team was able to successfully collect raw data from the STM which can be processed

9. Conclusion

 \succ Built with a budget of \$500 for academic use. \succ Will remain in the Complex Systems Laboratory at NAU for research on nano-

10. Future work

Investigation into using the system for nano- \succ Further improvements to the mechanical by improving the vibrational

11. Acknowledgements